



**JCEI'S**  
**JAIHIND COLLEGE OF ENGINEERING, KURAN**  
 Affiliated to Savitribai Phule Pune University, Pune  
 AN ISO 14001:2004 EMS & An ISO:9001:2015 QMS CERTIFIED  
 INSTITUTE

**TIME TABLE**  
 (ACADEMIC YEAR -2022-23)

**DEPARTMENT:- Electronics & Telecommunication Engg**

**CLASS :- S.E.**

**NAME OF CLASS TEACHER:- Prof.Chaugule S. D.**

**DIV:**

**SEMESTER: I**

**W.E.F. :- DATE: 12/09/2022**

**BLOCK / CLASS NO:- 10**

TIME / DAY	MON	TUE	WED	THU	FRI	SAT
09.15-10:15	M-III	DS	M-III	DS	Foreign Language	DC
10:15-11:15	EXC	EXC	EXC	EXC	M-III	DC
11:15-12:00	RECESS					
12.00-01:00	DS	M-III	ELC	ELC	M-III -TUT	DC
01.00-02:00	M-III	ELC	DS	M-III	ELC	DC
2.00-2:15	RECESS					
2.15-4:15	EXC S1 ELC S2	ESD S1 DS S2	ELC S1 EXC S2	MAC-3	DS S1 ESD S2	DC S1-S2


**Batches :- S1 :- 1-15      S2- 16-31**






Name Of Subject	TH/PR/OR/TW	Abbreviation	Subject Code	CLASS/LAB NO.	Name of Subject Teacher	Guardian for Roll Nos.
Engineering Maths-III	TH/TW	MIII	207005	S010	Prof.Kute A. D.	
Electronic Circuits	TH	EXC	204181	S010	Dr.Muljkar R.M.	
Digital Circuits	TH	DC	204182	S010	Prof.Gholap V.J.	
Electrical Cicuits	TH	ELC	204183	S010	Prof.Dhobale S. D.	
Data Structure	TH	DS	204184	S010	Prof. Auti M. A.	
Electronic CircuitsLab	PR	EXC Lab	204185	S002	Prof. Chaugule Sw. D.	
Digital Circuits Lab	PR	DC Lab	204186	S001	Prof.Gholap V.J.	
Electrical Circuits Lab	TW	ELC Lab	204187	G001	Prof.Dhobale S. D.	
Data Structure Lab	OR	DS Lab	204188	S022	Prof. Auti M. A.	
Electronic Skill Development	TW	ESD	204189	S007	Prof. Chaugule Sw. D.	
Mandatory Audit Course 3		MAC-3	204190	S007	Prof. Chaugule Sw. D.	

ISSUE	REVISION	PREPARED BY	VERIFIED BY	APPROVED BY	PAGE No	CHAPTER No.
No. : 00	No. : 03	DSGN.: T T INCHARGE	DSGN.: HOD	DSGN.: IQAC COORDINATOR	01 OF 01	LIST-JCOE-01
DATE: 12/09/2022	DATE: 12/09/2022	SIGN. <i>[Signature]</i>	SIGN. <i>[Signature]</i>	SIGN. <i>[Signature]</i>		

F/JCOE/AC/01/00/25/07/2022

**Head of Department**  
**(E & TC)**  
 JCEI's Jaihind College of Engineering,  
 Kuran, Tal. Junnar, Dist. Pune - 410511

		<b>JCEI'S</b> <b>JAIHIND COLLEGE OF ENGINEERING, KURAN</b> Affiliated to Savitribai Phule Pune University, Pune AN ISO 14001:2004 EMS & An ISO:9001:2015 QMS CERTIFIED <b>INSTITUTE</b>			<b>TIME TABLE</b> <b>(ACADEMIC YEAR -2022- 23 )</b>		
				<b>DEPARTMENT:- Electronics and Telecommunication Engineering</b>			
<b>CLASS :- T.E.</b>		<b>DIV:</b>	<b>SEMESTER:I</b>		<b>W.E.F. :- DATE:12/09/2022</b>		
<b>NAME OF CLASS TEACHER:-Prof. Satpute R. S.</b>					<b>BLOCK / CLASS NO:- 11</b>		
<b>TIME / DAY</b>	<b>MON</b>	<b>TUE</b>	<b>WED</b>	<b>THU</b>	<b>FRI</b>	<b>SAT</b>	
09.15-10:15	MC	EFT	EFT	EFT	Foreign Language	EFT	
10:15-11:15	DC	MC	JAVA	DBM	MC	EFT-TUT(T1/T2)	
11:15-12:00	RECESS						
12.00-01:00	JAVA	DC	DC	MAC-5	DBM	JAVA	
01.00-02:00	DBM	MC	Library	JAVA	DC	DBM	
2.00-2:15	RECESS						
2.15-3:15	DBM-T1	DC-T1	SD-T1/T2	JAVA-T1	MC-T1	EFT-TUT(T1/T2)	
3.15-4:15	MC-T2	DBM-T2		DC-T2	JAVA-T2	MAC-5	
<b>Batches :- T1 :-01 To 19      T2-20 to 37</b>							
<b>Name Of Subject</b>	<b>TH/PR/OR/TW</b>	<b>Abbreviation</b>	<b>Subject Code</b>	<b>CLASS /LAB No.</b>	<b>Name of Subject Teacher</b>	<b>Guardian for Roll Nos.</b>	
Digital Communication	TH	DC	304181	S011	Prof. Chaugule S. D.		
Electromagnetics field theory	TH	EFT	304182	S011	Dr. Mulajkar R. M.		
Database Managemet	TH	DBM	304183	S011	Prof. Auti M. A.		
Microcontrollers	TH	MC	304184	S011	Prof. Nalawade M. S.		
EL-I (FUNDAMENTAL OF JAVA)	TH	JAVA	304185	S011	Prof. Satpute R. S		
Digital Communication Lab	PR	DC Lab	304186	S002	Prof. Chaugule S. D.		
Database Managemet Lab	PR	DBM Lab	304187	S002	Prof. Auti M. A.		
Microcontrollers Lab	PR	MC Lab	304188	S007	Prof. Nalawade M. S.		
EL-I (JAVA) Lab	PR	Java Lab	304189	S020	Prof. Satpute R. S		
Skill Development	PR	SD Lab	304190	S007	Prof.Gholap V.J.		
Mandatory Audit Course 5		MAC-5	304191A	-	Prof. Satpute R. S		
<b>ISSUE</b>	<b>REVISION</b>	<b>PREPARED BY</b>	<b>VERIFIED BY</b>	<b>APPROVED BY</b>	<b>PAGE No</b>	<b>CHAPTER No.</b>	
No. : 00	No. : 03	DSGN.: T T INCHARGE	DSGN.: HOD	DSGN.: IQAC COORDINATOR	DSGN.: PRINCIPAL		
DATE: 12/09/2022	DATE:12/09/2022	SIGN. <i>[Signature]</i>	SIGN. <i>[Signature]</i>	SIGN. <i>[Signature]</i>	SIGN. <i>[Signature]</i>	01 OF 01	
<b>F:JCOE/AC/01/00/25/07/2022</b>		<b>(E &amp; TC)</b>		<b>Principal</b>		<b>LIST-JCOE-01</b>	
<b>JCEI's Jaihind College of Engineering, Kuran, Tal. Junnar, Dist. Pune</b>				<b>JCEI's Jaihind College Of Engineering, Kuran, Tal. Junnar, Dist. Pune - 410511</b>			

	<b>JCEI'S</b> <b>JAIHIND COLLEGE OF ENGINEERING, KURAN</b> Affiliated to Savitribai Phule Pune University, Pune AN ISO 14001:2004 EMS & An ISO:9001:2015 QMS CERTIFIED <b>INSTITUTE</b>				<b>TIME TABLE</b> <b>(ACADEMIC YEAR -2022- 23)</b>	
					<b>DEPARTMENT:- Electronics and Telecommunication Engineering</b>	
<b>CLASS :- B.E.</b>		<b>DIV:</b>	<b>SEMESTER: I</b>		<b>W.E.F. :- DATE: 12/09/2022</b>	
<b>NAME OF CLASS TEACHER:- Prof. Nalawade M. S.</b>					<b>BLOCK / CLASS NO:- 12</b>	
<b>TIME / DAY</b>	<b>MON</b>	<b>TUE</b>	<b>WED</b>	<b>THU</b>	<b>FRI</b>	<b>SAT</b>
09.15-10:15	CC	IOT	VLSI	CC	Foreign Language	IOT
10:15-11:15	RMT	EPD	EPD	VLSI	IOT	VLSI
11:15-12:00	<b>RECESS</b>					
12.00-01:00	IOT	CC	VLSI-B1	IOT-B1	EPD	RMT
01.00-02:00	EPD	RMT	IOT-B2	VLSI-B2	VLSI	Library
2.00-2:15	<b>RECESS</b>					
2.15-3:15	CC-B1 RMT-B2	RMT-B1 CC-B2	CC	MAC-7	PP-B1/B2	PP-B1/B2
3.15-4:15			RMT			
<b>Batches :- B1 :-01 To 15      B2-16 to 26</b>						
<b>Name Of Subject</b>	<b>TH/PR/OR/ TW</b>	<b>Abbreviation</b>	<b>Subject Code</b>	<b>CLASS/Lab No.</b>	<b>Name of Subject Teacher</b>	
Radiation & Microwave Techniques	TH	RMT	404181	S012	Dr. Mrs. Dhede V.M	
VLSI Design & Technique	TH	VLSI	404182	S012	Prof. Nalawade M. S.	
Cloud computing	TH	CC	404183	S012	Prof. Satpute R. S	
Modernized IOT	TH	IOT (EL-III)	404184	S012	Prof. Chaugule Sw. D.	
Electronic Product Design	TH	EPD(EL-IV)	404185	S012	Dr. Mrs. Dhede V.M	
Lab Practice-I (RMT+CC)	OR/TW	LP-I	404186	S001+S022	Dr. Mrs. Dhede V.M/Prof. Satpute R. S	
Lab Practice-II (VLSI+IOT(EL-III))	PR/TW	LP-II	404187	S020+S001	Prof. Nalawade M. S./ Prof. Chaugule Sw. D.	
Project Phase -I	OR	PP-I	404188	S007	Dr. Mrs. Dhede V.M, , Dr. Mulajkar R. M.	
Audit Course 7	-	AC	404189		Prof. Nalawade M. S.	
<b>ISSUE</b>	<b>REVISION</b>	<b>PREPARED BY</b>	<b>VERIFIED BY</b>	<b>APPROVED BY</b>		<b>CHAPTER No.</b>
No. : 00	No. : 03	DSGN.: T T INCHARGE	DSGN.: HOD	DSGN.: IQAC COORDINATOR	DSGN.: PRINCIPAL	01 OF 01
DATE: 12/09/2022	DATE: 12/09/2022	SIGN. 	SIGN. 	SIGN. 	SIGN. 	

F/JCOE/AC/01/00/25/07/2022

